

FORM PTO-1449  
REV. 7.801U.S. Department of Commerce  
PATENT AND TRADEMARK OFFICEAttorney Docket No.  
15.52/6357Serial No.  
10/025,347

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant:  
HOGYOKUFiling Date  
12/19/01Group  
2811

2822


## U.S. PATENT DOCUMENTS


Examiner Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date if Appropriate

## FOREIGN PATENT DOCUMENTS

FOREIGN-AREA DOCUMENTS											
	Document Number					Date	Country	Class	Sub-Class	Translation	
										Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Wei and Antoniadis, "Minimizing Floating-Body-Induced Threshold Voltage Variation in Partially Depleted SOI CMOS," <i>IEEE Electron Device Letters</i> , Vol. 17, No. 8 (August 1996) at pp. 391-394.

Examiner 	Date Considered 9/27/02
---	----------------------------

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

RECEIVED  
APR 17 2002  
TECHNOLOGY CENTER 2800